

# FORMATION OF UNDERLYING FILM/ AMORPHOUS SEMICONDUCTOR FILM

Fig.1(A)

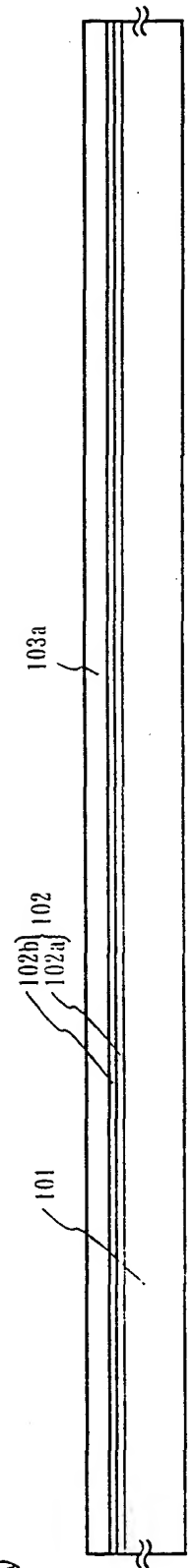
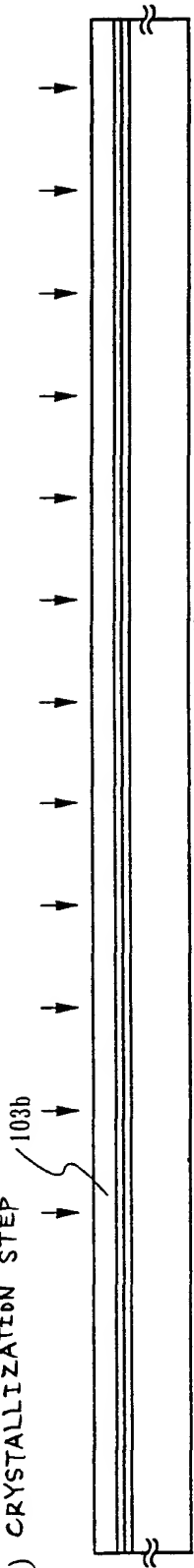
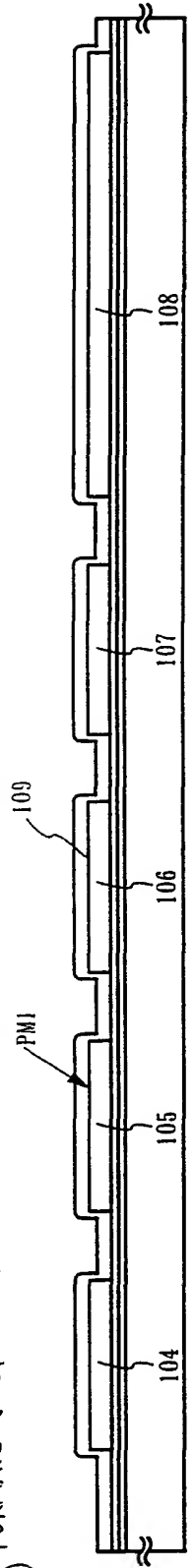


Fig.1(B) CRYSTALLIZATION STEP



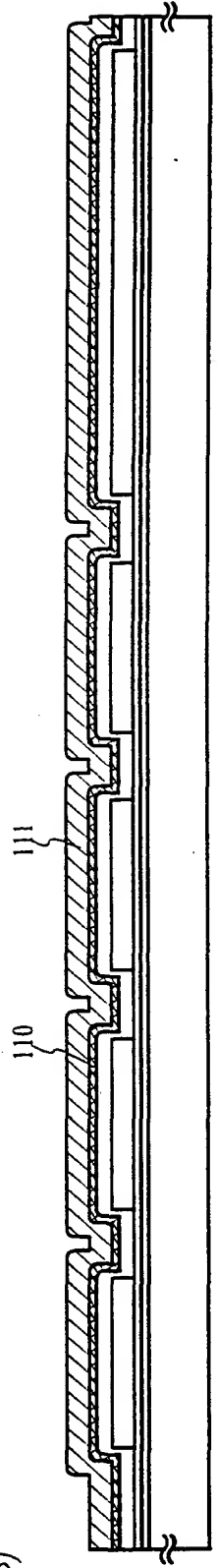
# FORMATION OF ISLAND SHAPE SEMICONDUCTOR LAYER/ GATE INSULATION FILM

Fig.1(c)



# FORMATION OF HEAT-RESISTANT CONDUCTOR LAYER

Fig.1(b)



# FORMATION OF GATE ELECTRODE

Fig. 2(A)

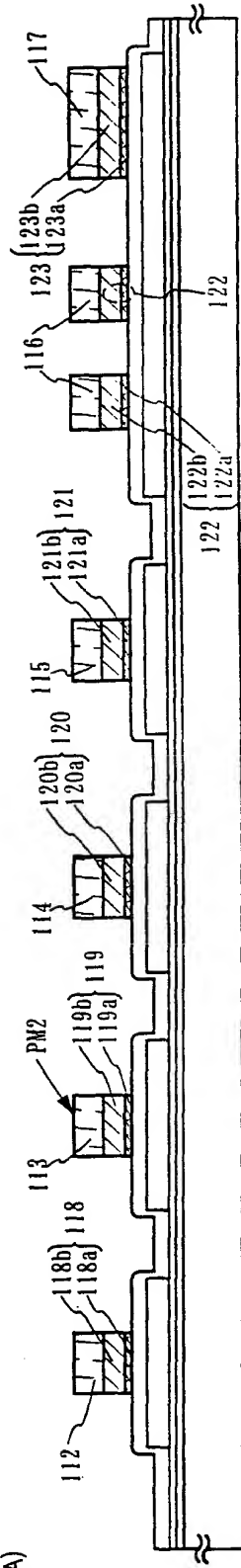


Fig. 2(B) n<sup>+</sup> DOPING STEP

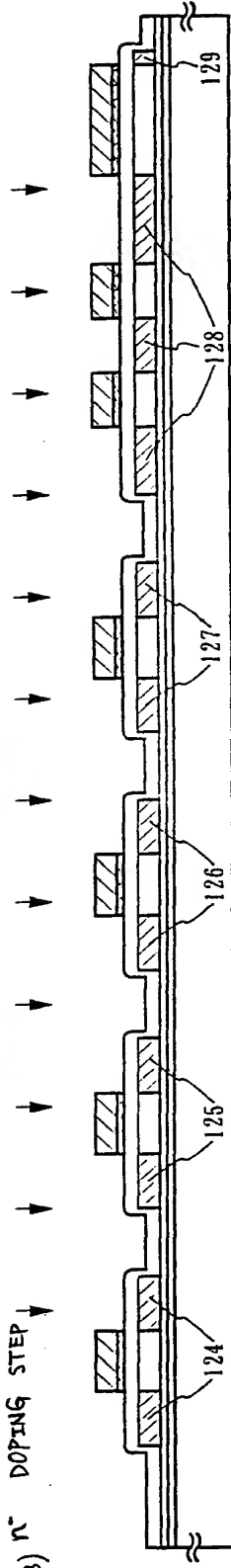


Fig. 2(C) n<sup>+</sup> DOPING STEP

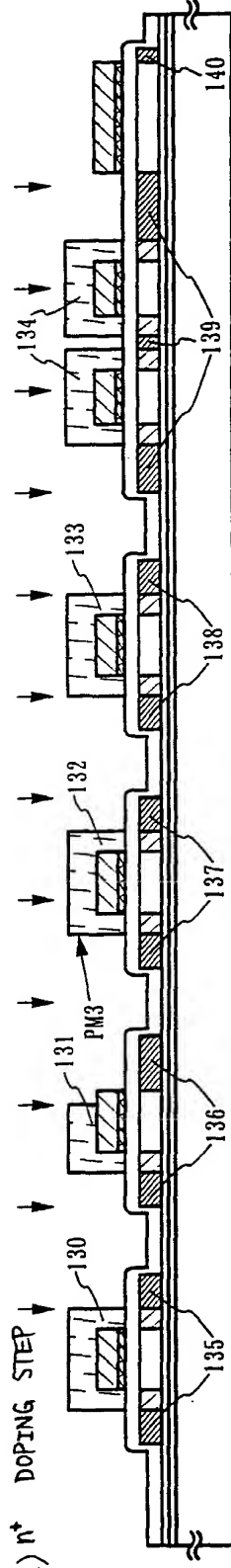


Fig. 2(D) p<sup>+</sup> DOPING STEP

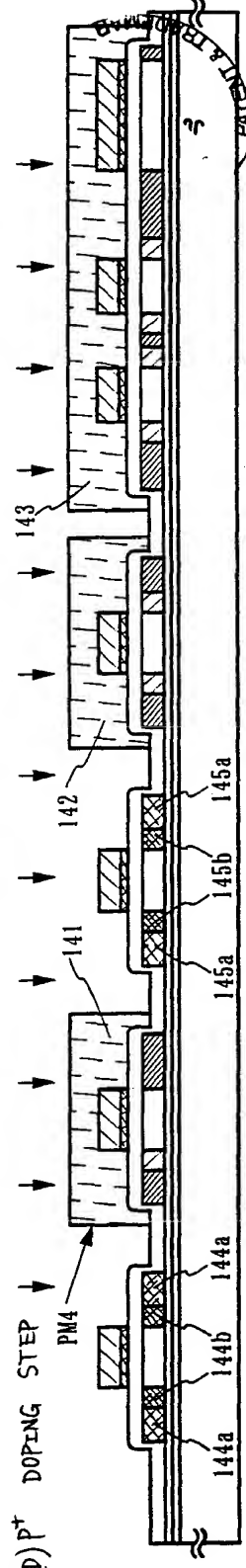


Fig. 3(A) FORMATION OF PROTECTIVE INSULATION FILM

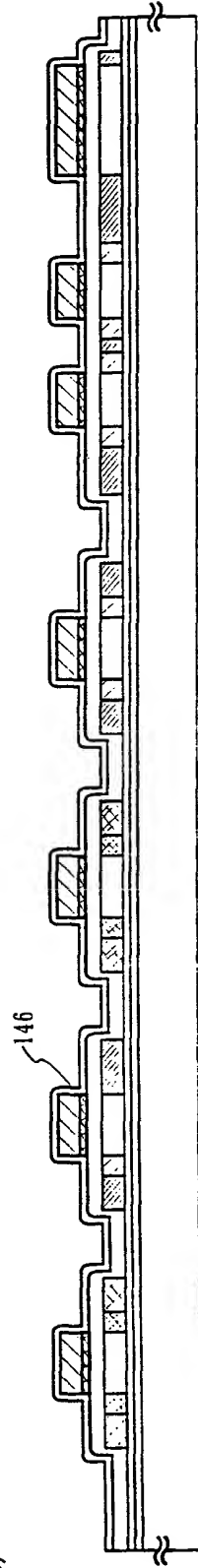


Fig. 3(B) ACTIVATION STEP

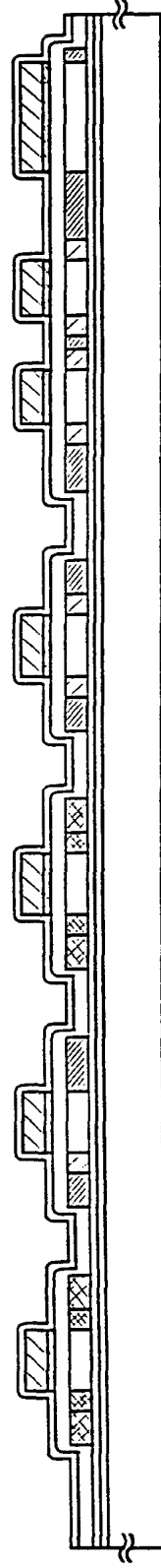


Fig. 3(C) FORMATION OF RESIN INTER-LAYER INSULATION FILM / PERFORATION OF CONTACT HOLE / FORMATION OF LEAD WIRE AND PIXEL ELECTRODE

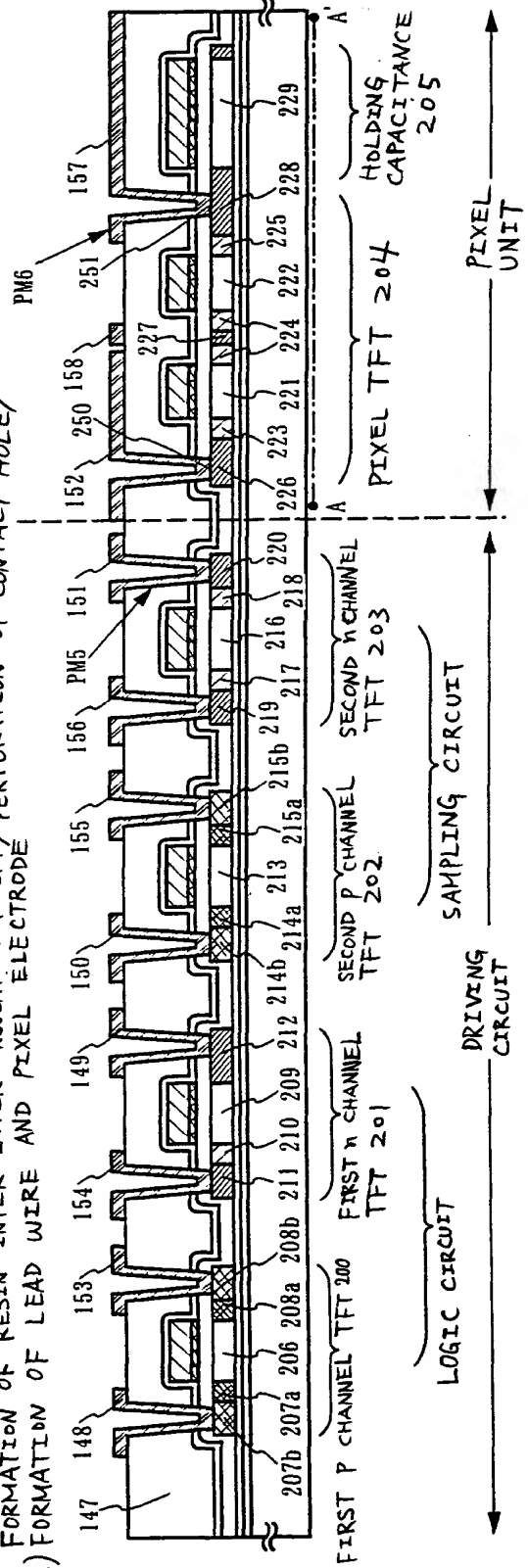


Fig. 4(A)  $n^-$  DOPING STEP

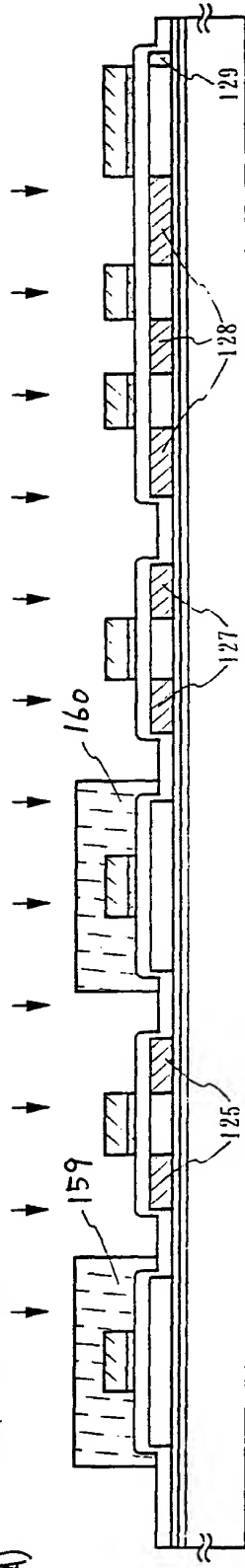


Fig. 4(B)  $n^+$  DOPING STEP

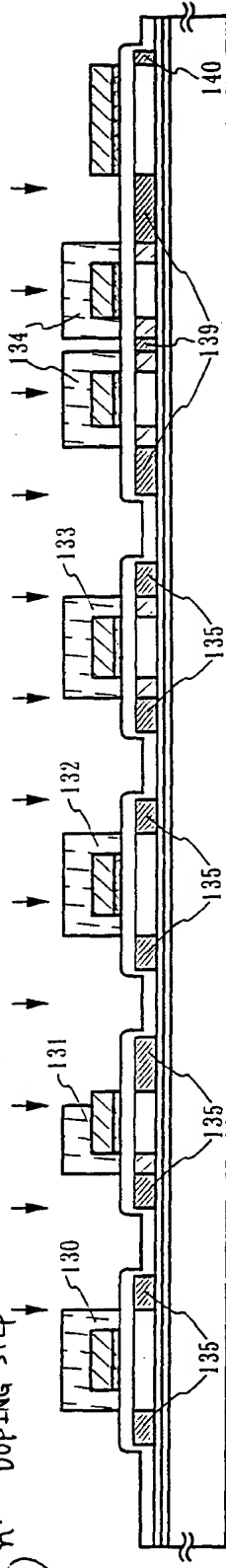


Fig. 4(C)  $P^+$  DOPING STEP

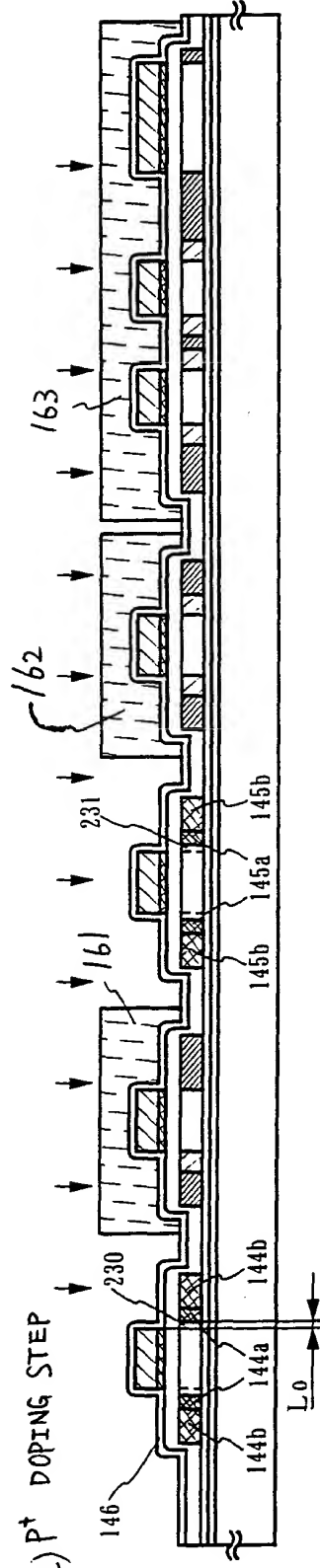


Fig. 5(A) ACTIVATION STEP

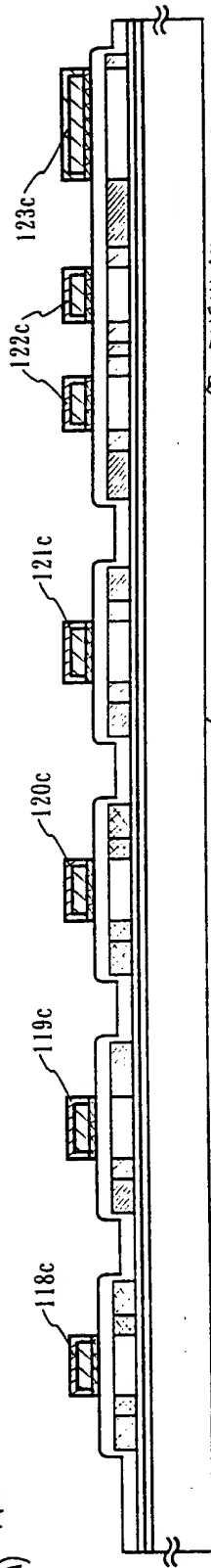


Fig. 5(B) FORMATION OF LOW RESISTANCE CONDUCTIVE LAYER / FORMATION OF PROTECTIVE INSULATION FILM

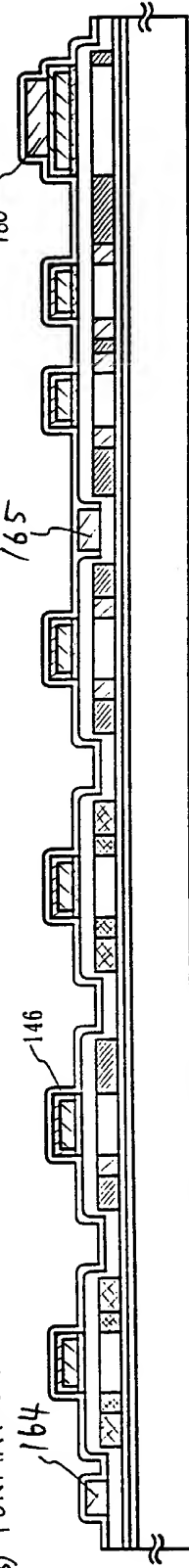
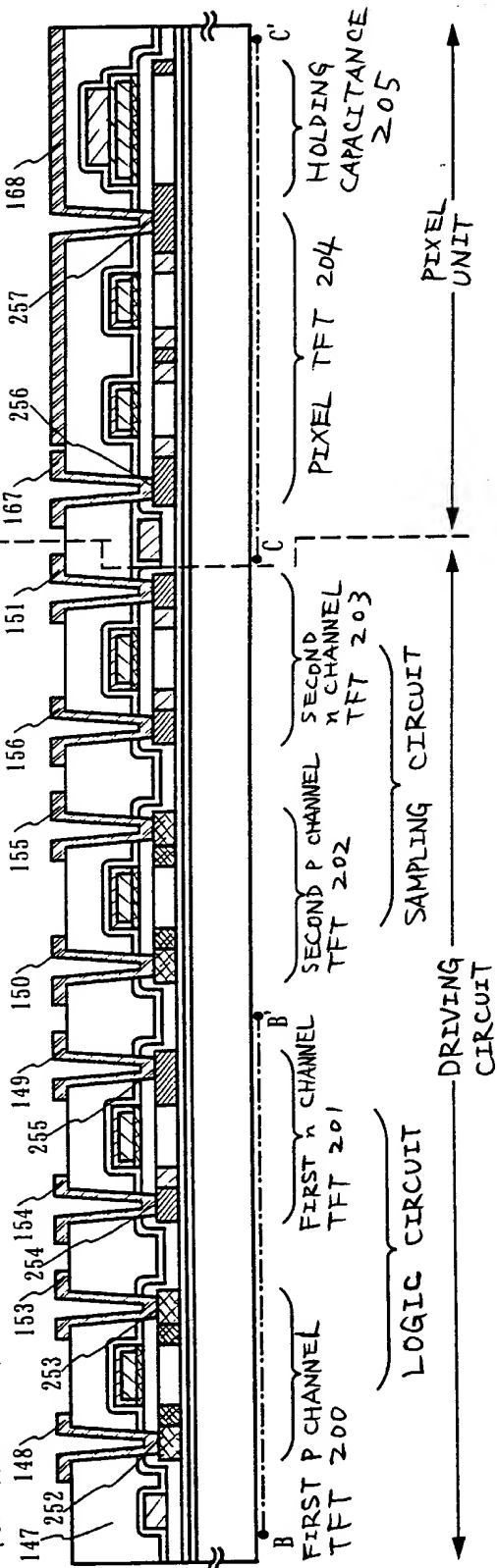


Fig. 5(C) FORMATION OF RESIN INTER-LAYER INSULATION FILM / PERFORATION OF CONTACT HOLE / FORMATION OF LEAD WIRE AND PIXEL ELECTRODE



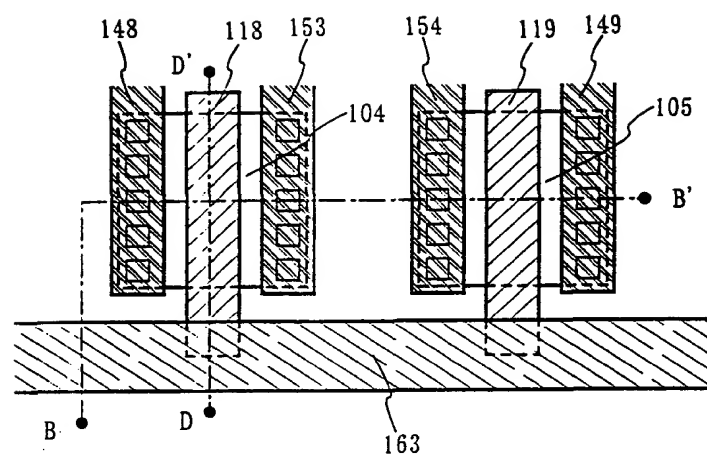


Fig. 6(A)

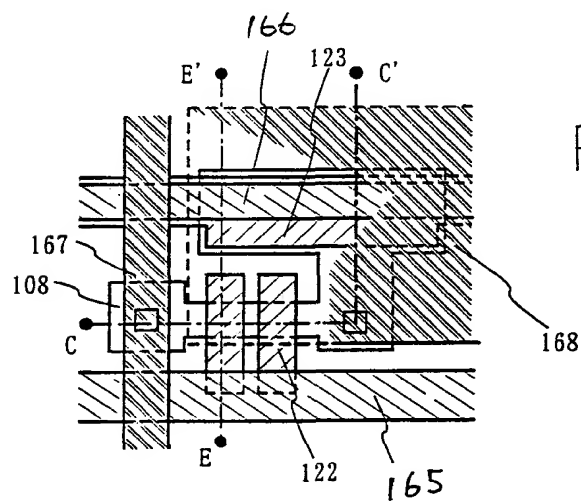


Fig. 6(B)

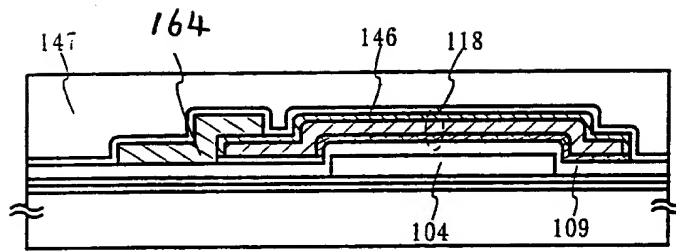


Fig. 7(A)

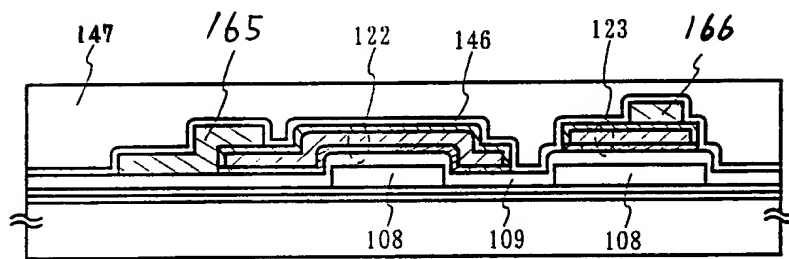


Fig. 7(B)

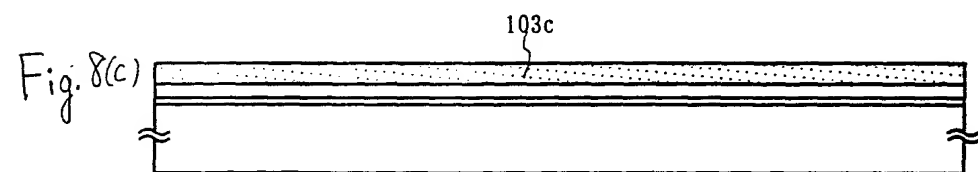
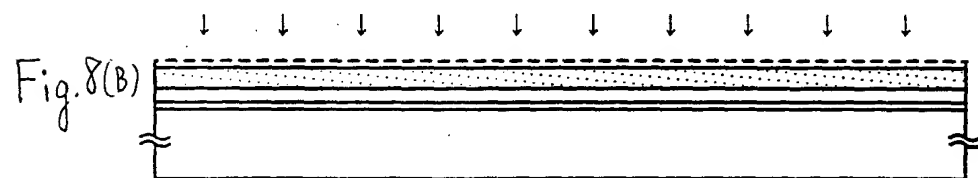
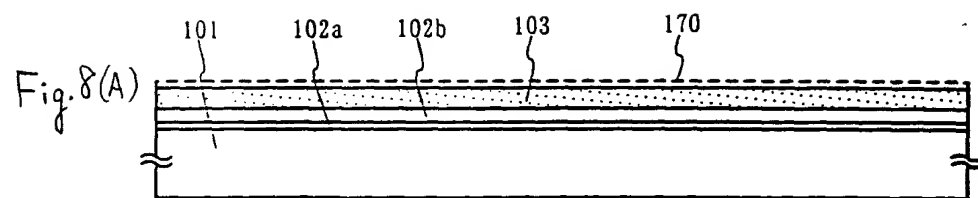




Fig. 9 ACTIVATION

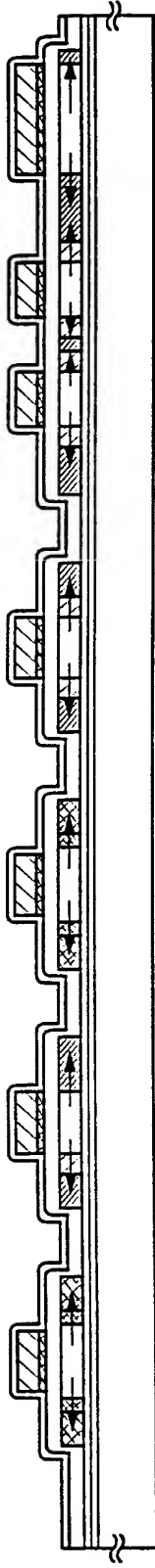


Fig. 10(A)

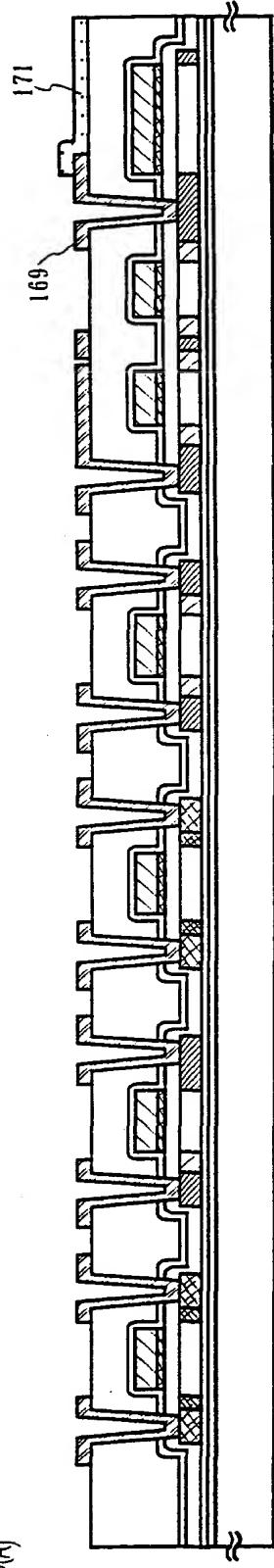
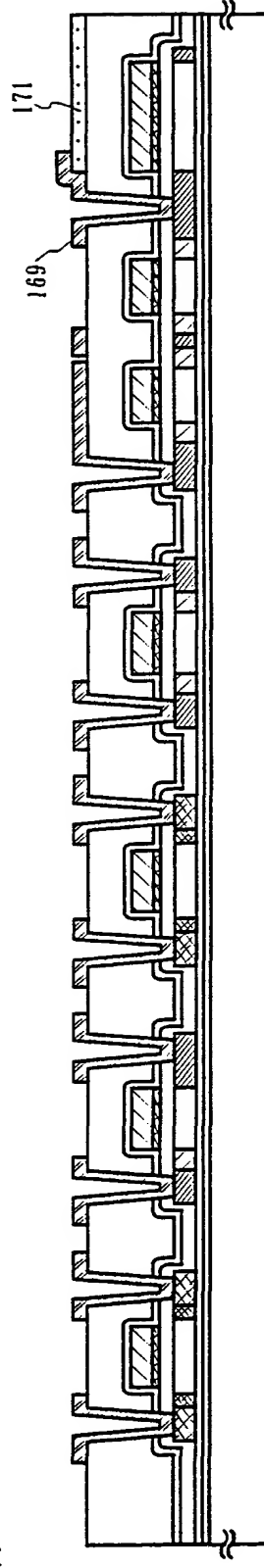


Fig. 10(B)



FORMATION OF ORIENTATION FILM / FORMATION OF SPACER /

Fig. 11(A) RUBBING TREATMENT

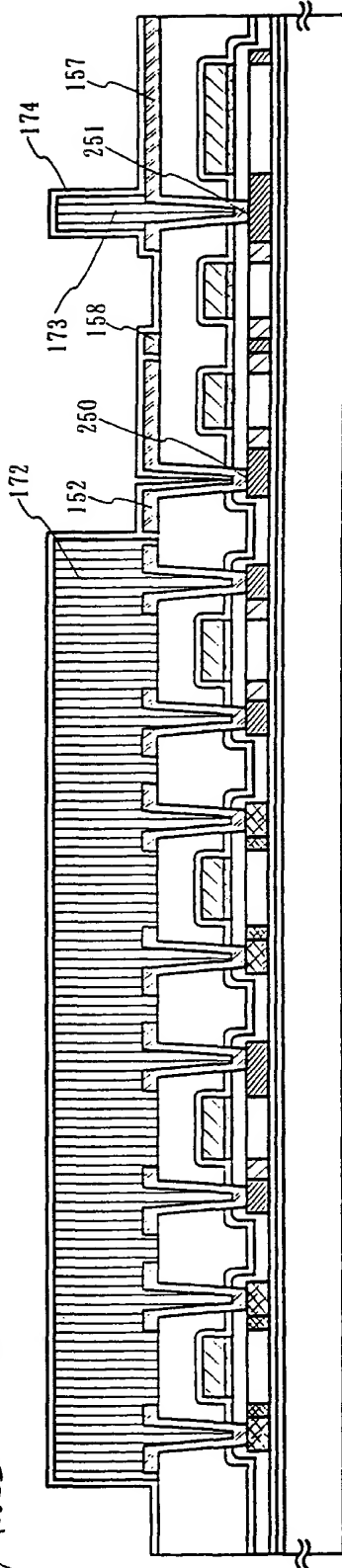
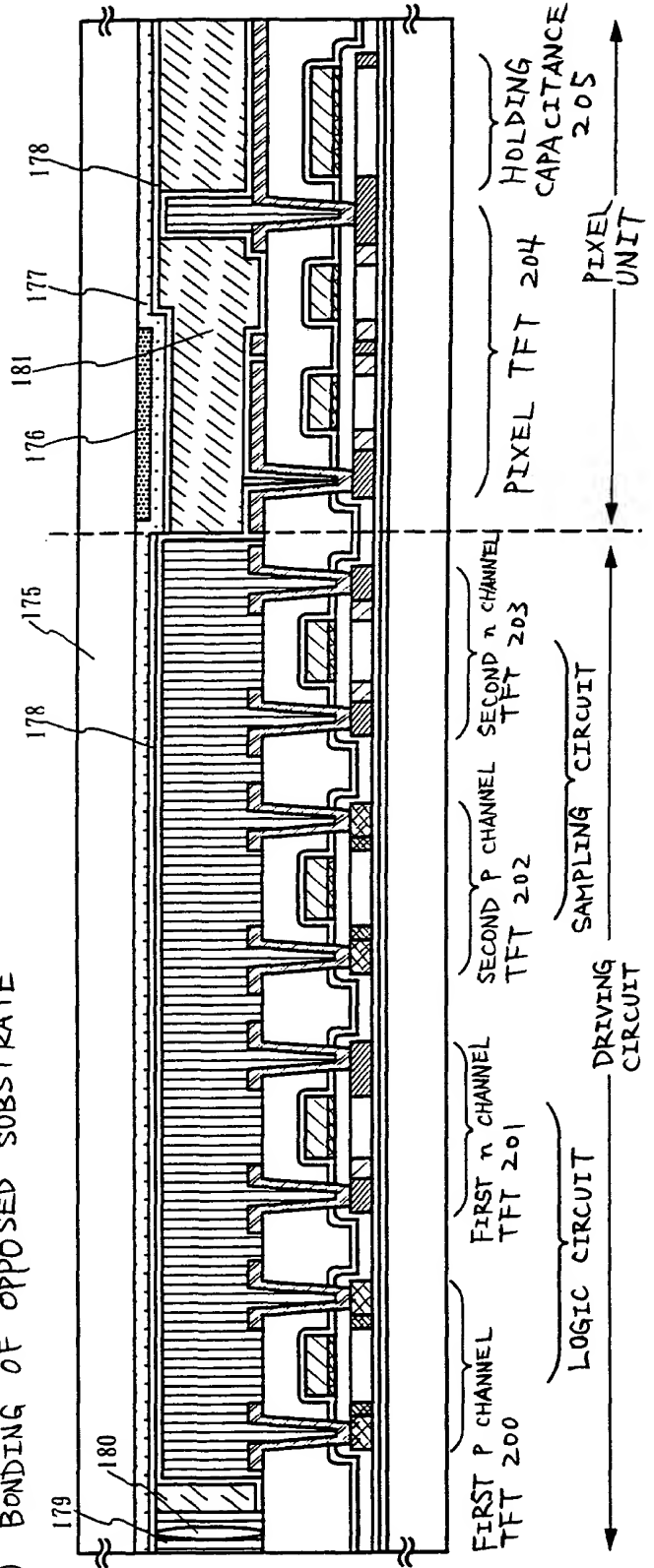


Fig. 11(B) BONDING OF OPPOSED SUBSTRATE



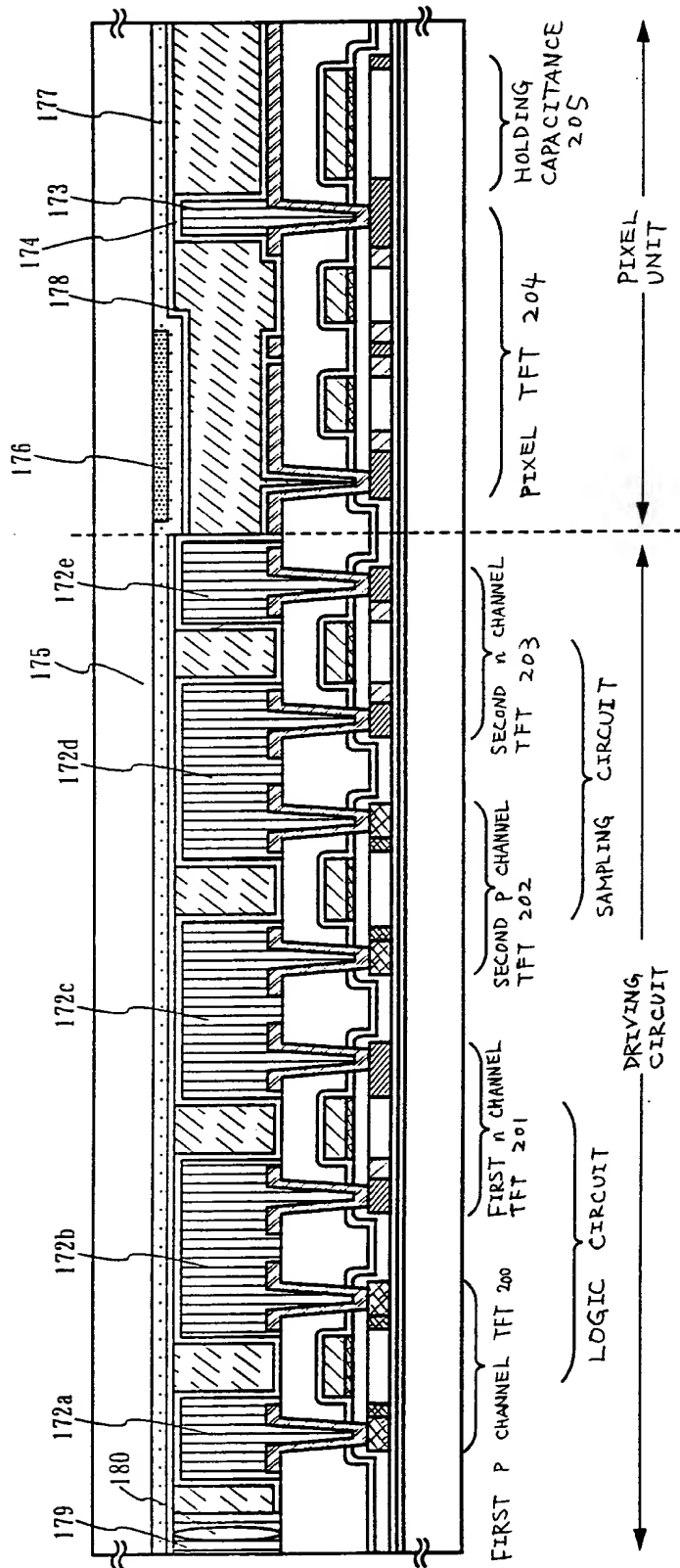
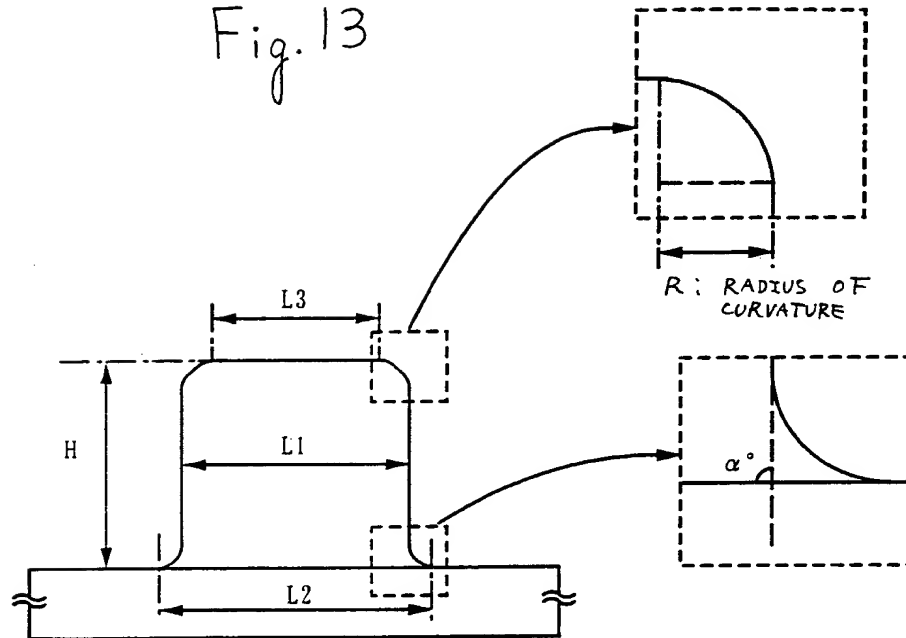


Fig. 12

Fig. 13



SECTIONAL TYPICAL VIEW  
OF SPACER

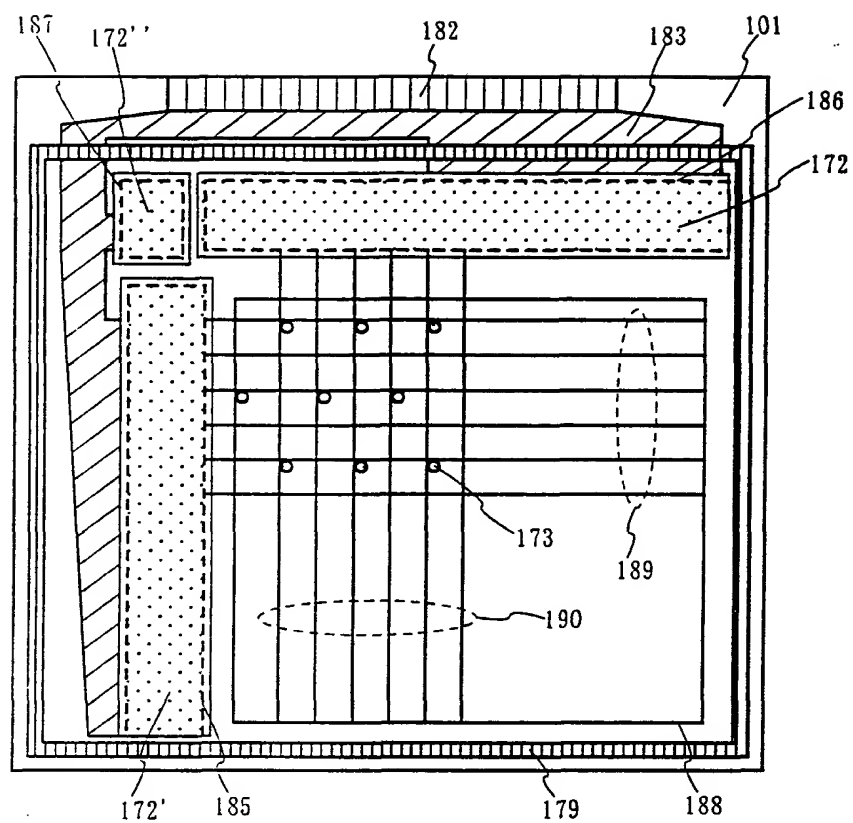


Fig. 14

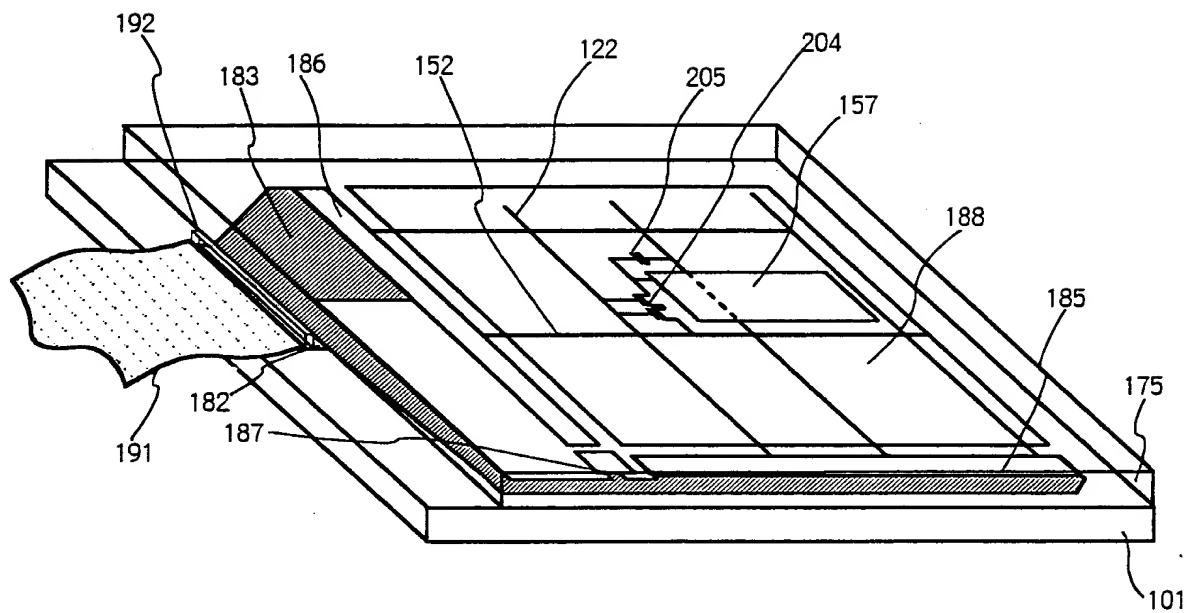


Fig. 15

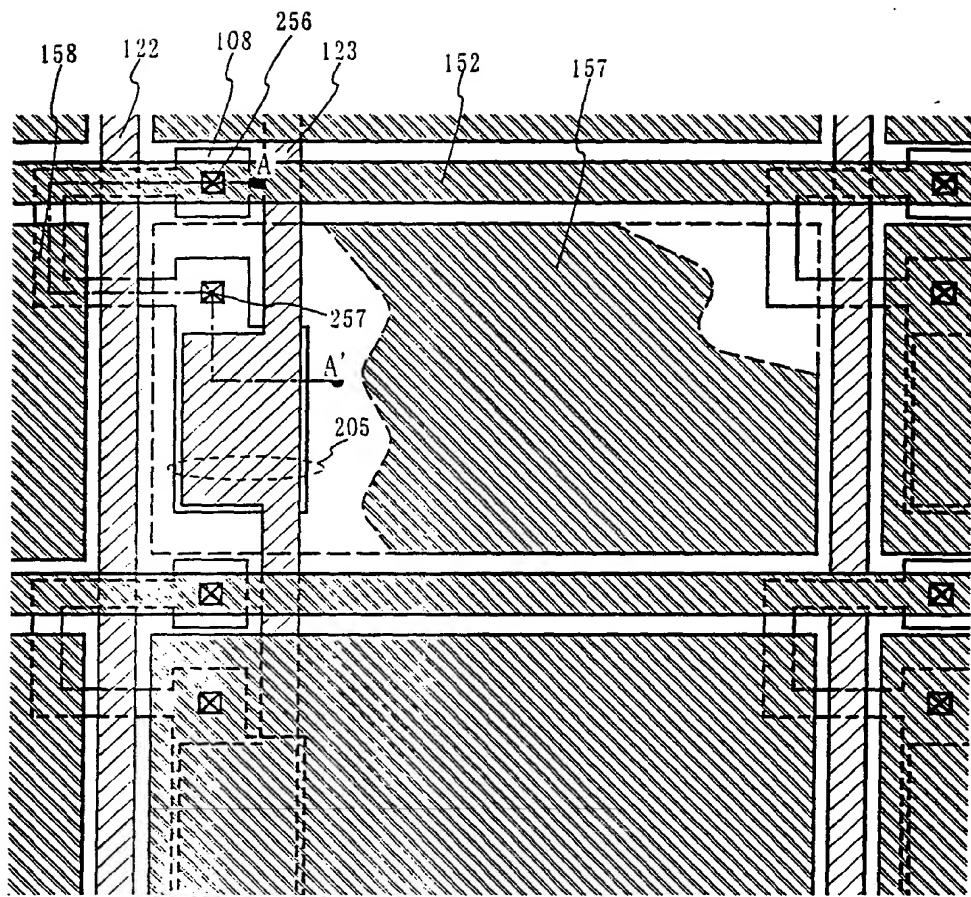


Fig. 16



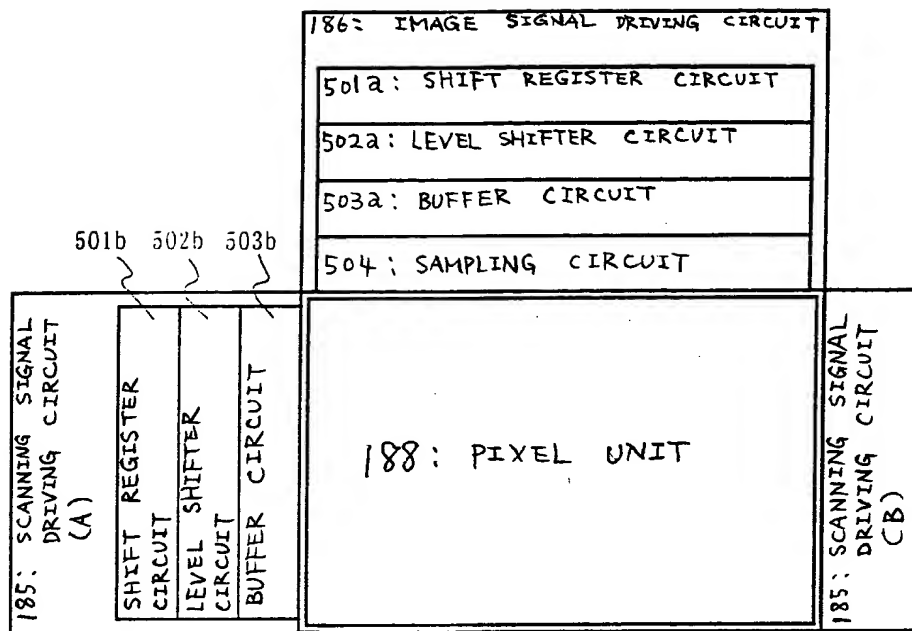


Fig. 17

Fig. 18

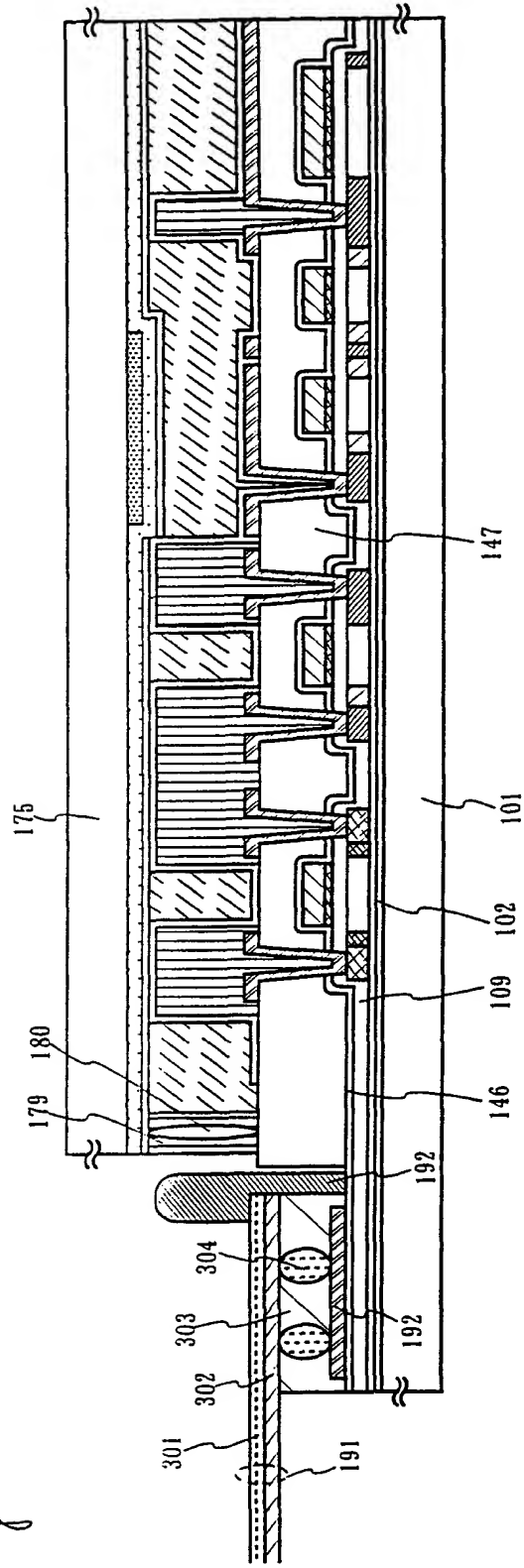


Fig. 19

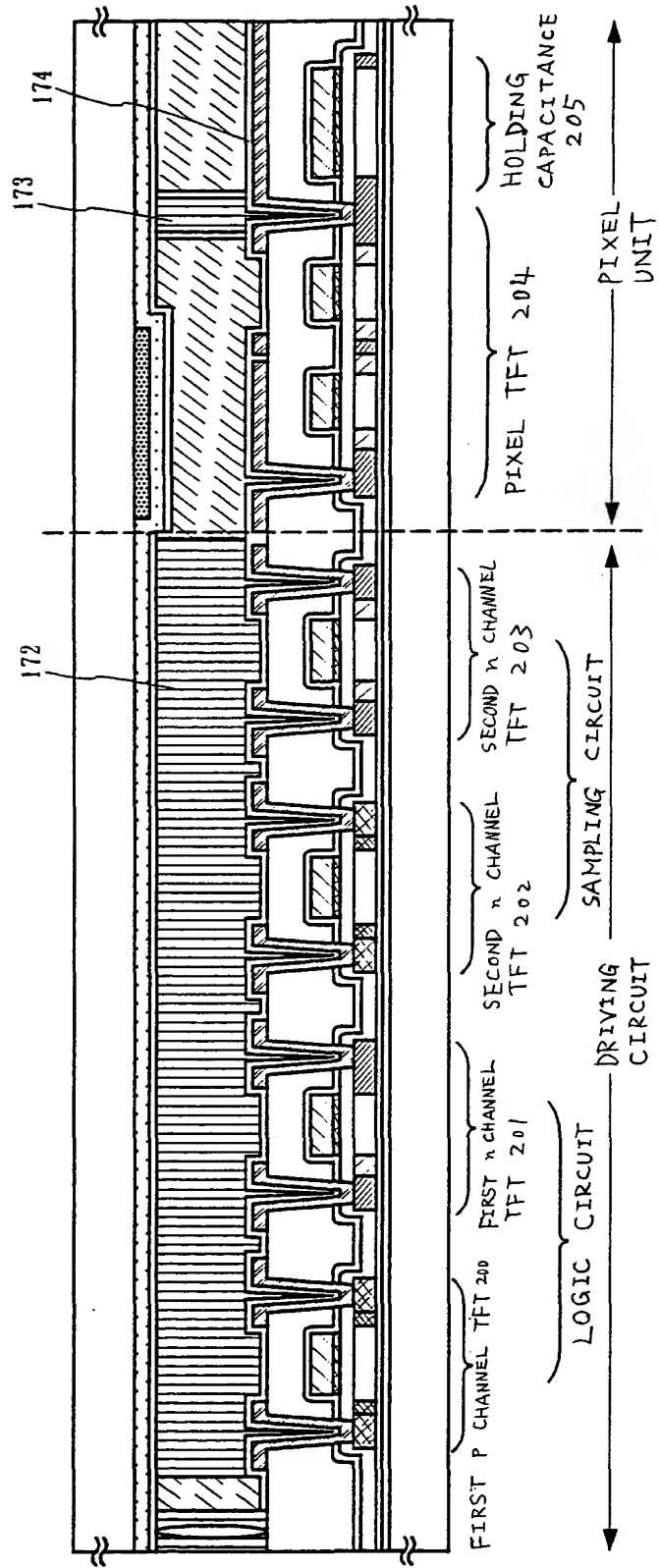
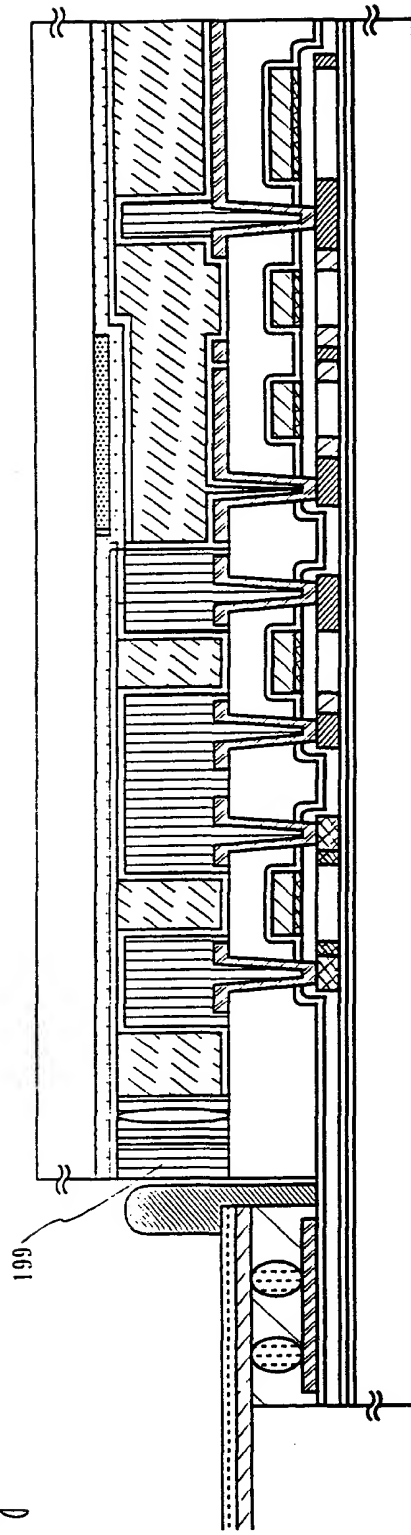
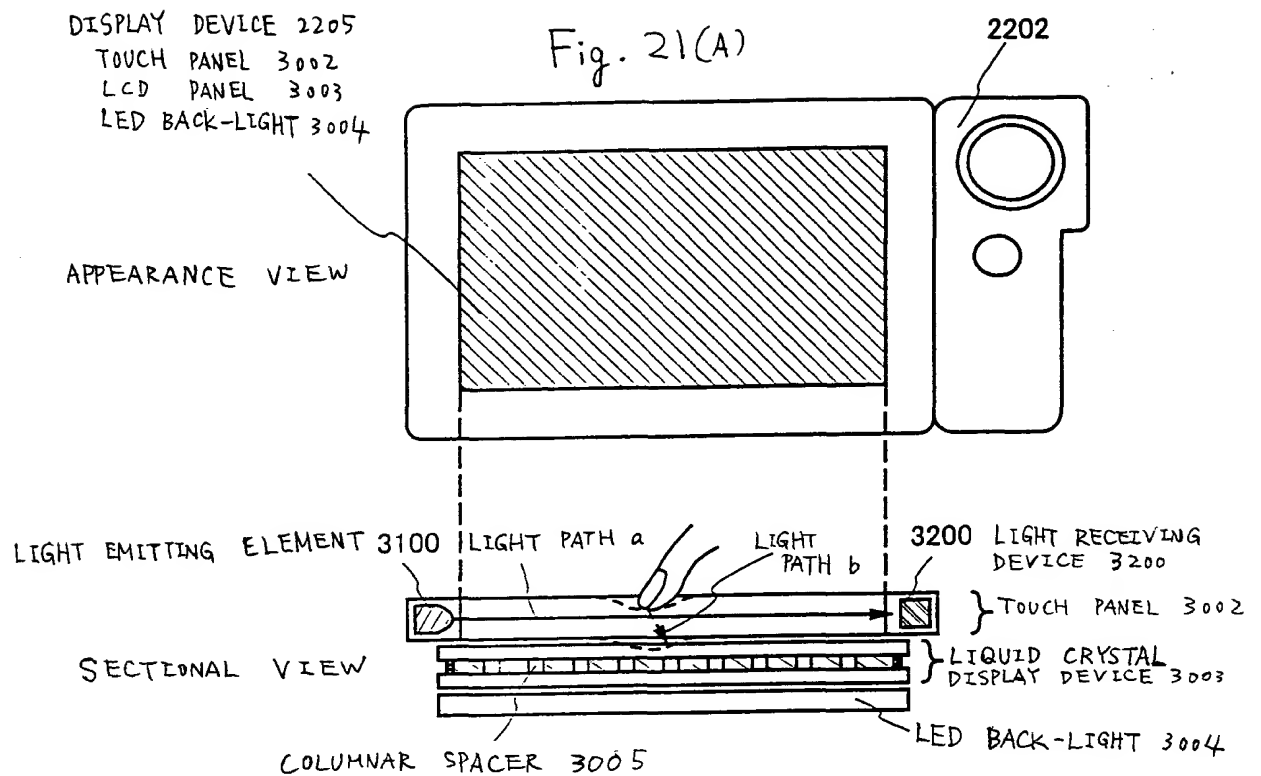
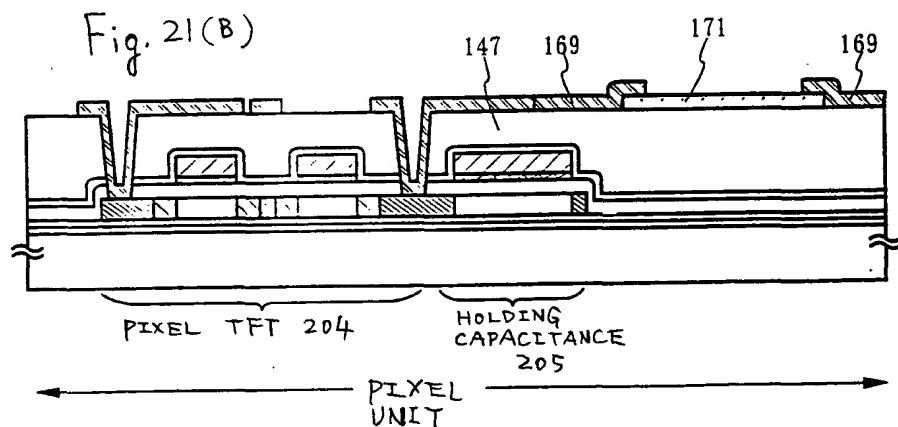


Fig. 20





APPEARANCE VIEW AND SECTIONAL VIEW OF PORTABLE INFORMATION  
TERMINAL (OPTICAL TOUCH PANEL)



SECTIONAL VIEW OF PIXEL UNIT

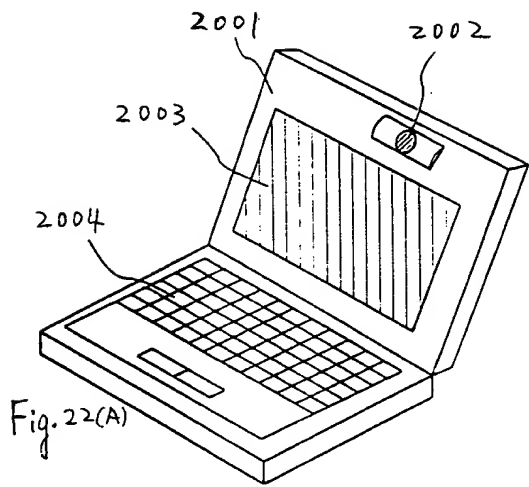


Fig. 22(A)

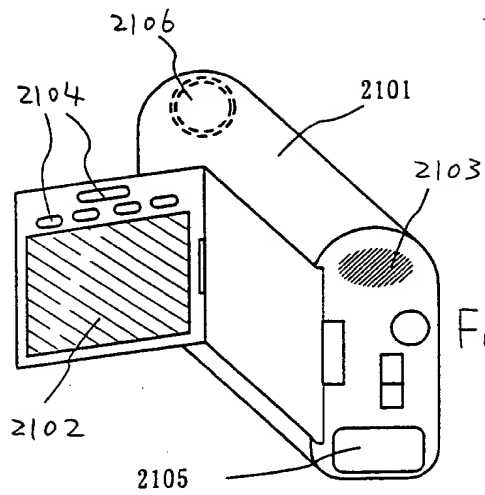


Fig. 22(B)

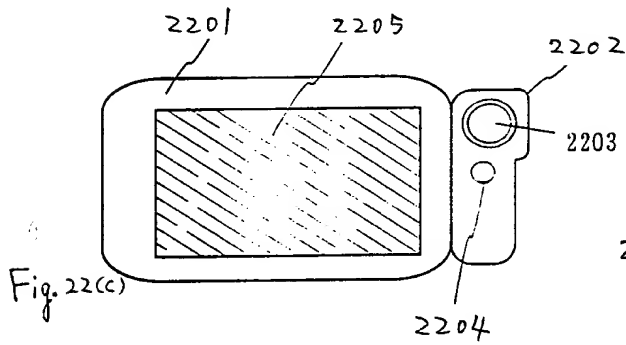


Fig. 22(C)

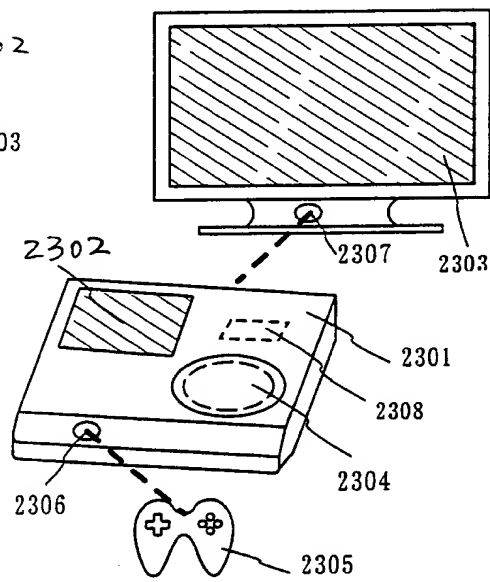


Fig. 22(D)

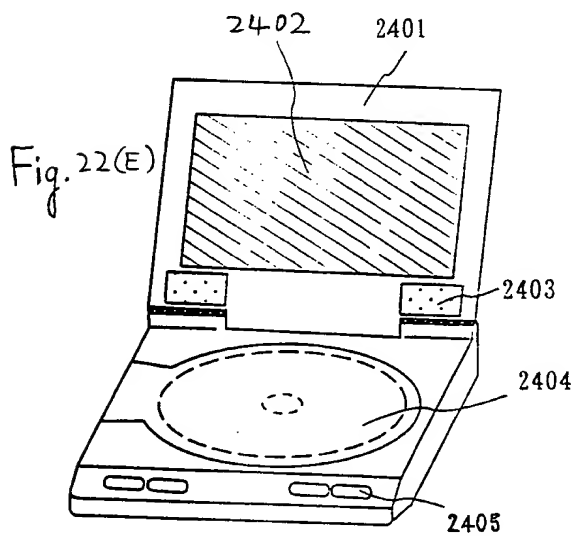


Fig. 22(E)

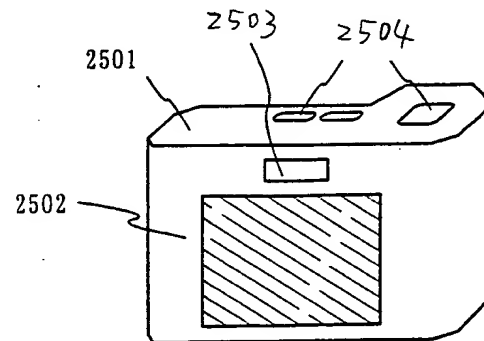


Fig. 22(F)

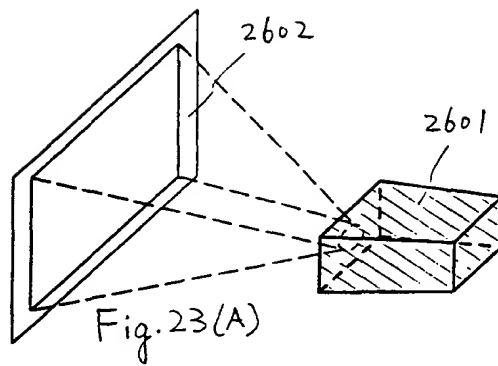


Fig. 23(A)

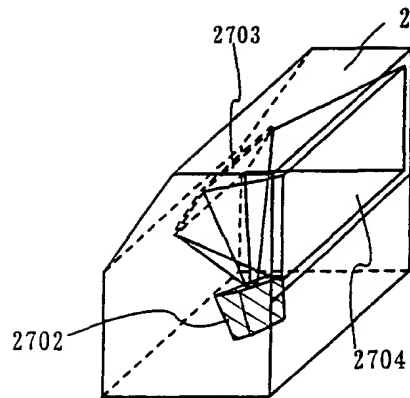


Fig. 23(B)

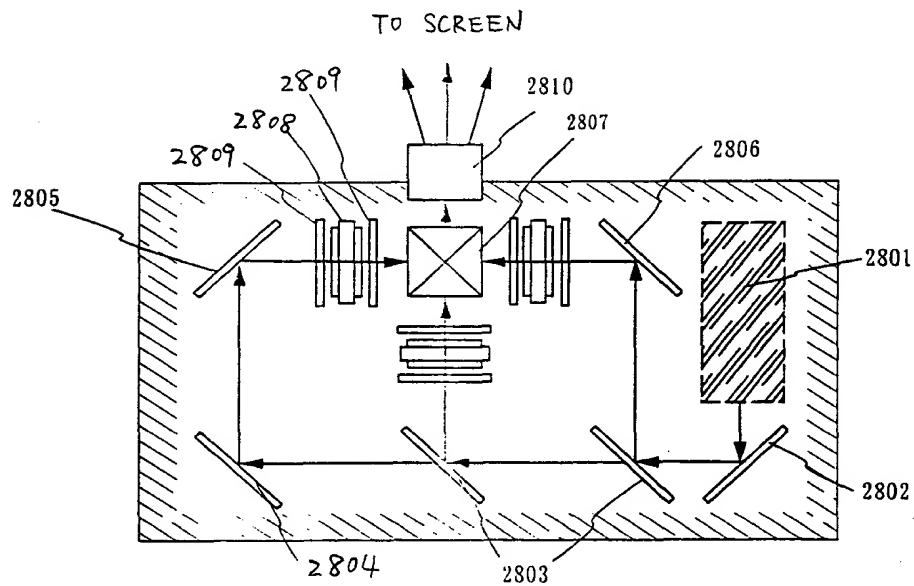


Fig. 23(C) LIGHT SOURCE OPTICAL SYSTEM AND DISPLAY DEVICE (THREE PLATE TYPE)

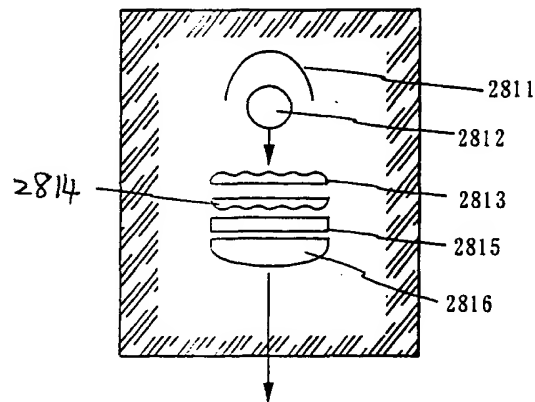


Fig. 23(D)

LIGHT SOURCE  
OPTICAL SYSTEM